



Inventor Name Search Result

Your Search was:

Last Name = KWA

First Name = SEH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09968473	7020724	150	09/28/2001	ENHANCED POWER REDUCTION CAPABILITIES FOR STREAMING DIRECT MEMORY ACCESS ENGINE	KWA, SEH
10002333	Not Issued	71	11/14/2001	Computing system and method to select data packet	KWA, SEH
10882544	Not Issued	41	06/30/2004	Dynamic lane, voltage and frequency adjustment for serial interconnect	KWA, SEH
11173220	Not Issued	30	06/30/2005	Power management system for computing platform	KWA, SEH
11173784	Not Issued	30	06/30/2005	Various methods and apparatuses for power states in a controller	KWA, SEH
11329328	Not Issued	30	01/09/2006	Enhanced power reduction capabilities for streaming direct memory access engine	KWA, SEH
10629967	Not Issued	71	07/30/2003	Interference mitigation by adjustment of interconnect transmission characteristics	KWA, SEH W.
10749855	Not Issued	71	12/30/2003	Method and an apparatus for power management in a computer system	KWA, SEH W.
11166646	Not Issued	30	06/23/2005	Method and system for deterministic throttling for thermal management	KWA, SEH W.
11384681	Not Issued	20	03/20/2006	Providing a deterministic window for an idle state	KWA, SEH W.
10158654	Not Issued	161	05/30/2002	Integrated thermoelectric power generator and catalytic converter	KWAK, SEHOON
10205661	Not Issued	161	07/24/2002	Multi-phase suspension coolant	KWAK, SEHOON
10603476	Not Issued	41	06/25/2003	Battery thermal management with phase transition	KWAK, SEHOON
09128283	Not Issued	161	08/03/1998	COGNITION ORIENTED EMERGENT BEHAVIOR SYSTEM CONTROLLER	KWAK, SEHUNG DAVID

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Last Name = SRITANYARATANA

First Name = SIRIPONG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07284899	Not Issued	169	12/15/1988	COMPUTER MEMORY HAVING ITS OUTPUT LINES SELECTED FOR CONNECTION TO A DATA BUS BY THE MEMORY ADDRESS	SRITANYARATANA, SIRIPONG
07329528	Not Issued	161	03/28/1989	COMPUTER MEMORY FOR MINIMIZING NUMBER OF MEMORY CHIPS USED AND MAXIMIZING THEIR UTILIZATION	SRITANYARATANA, SIRIPONG
09216013	6452610	150	12/16/1998	METHOD AND APPARATUS FOR DISPLAYING GRAPHICS BASED ON FRAME SELECTION INDICATORS	SRITANYARATANA, SIRIPONG
09410380	6490703	150	09/30/1999	BUS POWER SAVINGS USING SELECTIVE INVERSION IN AN ECC SYSTEM	SRITANYARATANA, SIRIPONG
09534193	6633987	150	03/24/2000	METHOD AND APPARATUS TO IMPLEMENT THE ACPI C3 STATE IN A RDRAM BASED SYSTEM	SRITANYARATANA, SIRIPONG
09675493	6775785	150	09/29/2000	METHOD AND APPARATUS FOR ACCESS TO RESOURCES NOT MAPPED TO AN AUTONOMOUS SUBSYSTEM IN A COMPUTER BASED SYSTEM WITHOUT INVOLVEMENT OF THE MAIN OPERATING SYSTEM	SRITANYARATANA, SIRIPONG
09746086	6735659	150	12/21/2000	METHOD AND APPARATUS FOR SERIAL COMMUNICATION WITH A CO-PROCESSOR	SRITANYARATANA, SIRIPONG
09751427	Not Issued	161	12/29/2000	Method and apparatus to permit a peripheral device to become the default system bus master	SRITANYARATANA, SIRIPONG
09752627	6748548	150	12/29/2000	COMPUTER PERIPHERAL DEVICE THAT REMAINS OPERABLE WHEN CENTRAL PROCESSOR OPERATIONS ARE SUSPENDED	SRITANYARATANA, SIRIPONG
10091328	Not Issued	71	03/05/2002	Partially integrating wireless components of processor-based systems	SRITANYARATANA, SIRIPONG
10236338	6732288	150	09/06/2002	BUS POWER SAVINGS USING	SRITANYARATANA,

				SELECTIVE INVERSION IN AN ECC SYSTEM	SIRIPONG
<u>10331499</u>	Not Issued	93	12/30/2002	METHOD, APPARATUS AND ARTICLE FOR DISPLAY UNIT POWER MANAGEMENT	SRITANYARATANA, SIRIPONG
<u>10340020</u>	<u>6971034</u>	150	01/09/2003	POWER/PERFORMANCE OPTIMIZED MEMORY CONTROLLER CONSIDERING PROCESSOR POWER STATES	SRITANYARATANA, SIRIPONG
<u>10749855</u>	Not Issued	71	12/30/2003	Method and an apparatus for power management in a computer system	SRITANYARATANA, SIRIPONG
<u>11074233</u>	Not Issued	30	03/05/2005	System and method of coherent data transfer during processor idle states	SRITANYARATANA, SIRIPONG

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L2	26881	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:57
L3	1	I2 and (((transfer\$4 or transaction) near2 (based or depend\$4))with ((power or energy)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4)))same (threshold or limit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:50
L4	1	I2 and ((incoherent adj transaction)near3 (pend\$4 or queu\$4 or wait\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:46
L5	1	I2 and ((incoherent adj transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:44
L6	1	((incoherent adj transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:44
L7	89	((incoherent or non\$coherent)adj transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:58

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L8	1	I2 and (((incoherent or non\$coherent)adj transaction))with (inactivat\$4 or de\$assert\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:46
L9	11	I7 with ((pend\$4 or queu\$4 or wait\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:59
L10	1	I7 and (((power or energy)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4)))same (threshold or limit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:59
L11	46886	"709"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:57
L12	26654	"710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:57
L13	20	I11 and (((incoherent or non\$coherent)adj transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:58
L14	37	I12 and (((incoherent or non\$coherent)adj transaction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:58
L15	20	I13 and((pend\$4 or queu\$4 or wait\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:59

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L16	34	l14 and ((pend\$4 or queu\$4 or wait\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:59
L17	0	l15 and (((power or energy or voltage)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4)))same (threshold or limit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:00
L18	0	l16 and (((power or energy or voltage)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4)))same (threshold or limit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:00
L19	6	l16 and (((power or energy or voltage)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:01
L20	0	l17 and (((power or energy or voltage)near3 (manag\$4 or control\$4 or state or mode or adjust\$4 or switch\$4 or chang\$4))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:00
L21	3	("6763415").URPN.	USPAT	OR	ON	2006/08/09 16:02
L22	0	("6785758").URPN.	USPAT	OR	ON	2006/08/09 16:03
L23	0	("6813673").URPN.	USPAT	OR	ON	2006/08/09 16:03
L24	1	("6912611").URPN.	USPAT	OR	ON	2006/08/09 16:03



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James B. H. Kwa

January 1988 **ACM SIGART Bulletin**, Issue 103**Publisher:** ACM PressAdditional Information: [full citation](#), [citations](#), [index terms](#)2 [Tolerant planning and negotiation in generating coordinated movement plans in an automated factory](#)

J. B. H. Kwa

 June 1988 **Proceedings of the 1st international conference on Industrial and
 engineering applications of artificial intelligence and expert systems -
 Volume 1 IEA/AIE '88**
Publisher: ACM Press
 Full text available:  [pdf\(555.97 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Plan robustness is important for real world applications where modelling imperfections often result in execution deviations. The concept of tolerant planning is suggested as one of the ways to build robust plans. Tolerant planning achieves this aim by being tolerant of an agent's own execution deviations. When applied to multi-agent domains, it has the additional characteristic of being tolerant of other agents' deviant behaviour. Tolerant planning thus defers dynamic replanning until execu ...

3 [Texture detection for segmentation of iris images](#)

Asheer Kasar Bachoo, Jules-Raymond Tapamo

 July 2005 **Proceedings of the 2005 annual research conference of the South African
 institute of computer scientists and information technologists on IT
 research in developing countries SAICSIT '05**
Publisher: South African Institute for Computer Scientists and Information Technologists
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The idea of using the distinct spatial distribution of patterns in the human iris for person authentication is now a widely developing technology. Current systems rely on a set of basic assumptions in order to improve the accuracy and running time of the recognition process. The advent of a robust system implies a viable solution to a number of general problems. This paper focuses on a common yet difficult problem - the segmentation of eyelashes from iris texture. Tests give promising results wh ...

Keywords: GLCM, K-Means, classification, iris, localization, texture

4 Using structural representation of anomalous states of knowledge for choosing document retrieval strategies



N. J. Belkin, B. H. Kwaśnik

September 1986 **Proceedings of the 9th annual international ACM SIGIR conference on Research and development in information retrieval**

Publisher: ACM Press

Full text available: pdf(982.83 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

We report on a project which attempts to classify representations of the anomalous states of knowledge (ASKs) of users of document retrieval systems on the basis of structural characteristics of the representations, and which specifies different retrieval strategies and ranking mechanisms for each ASK class. The classification and retrieval strategy specification is based on 53 real problem statements, 35 of which have a total of 250 evaluated documents. Four facets of the ASK structures ha ...



5 Computer-generated design of electric circuits



Kwa-Sur Tam, Michael Besso, Renuka Racha

June 1988 **Proceedings of the 1st international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 IEA/AIE '88**

Publisher: ACM Press

Full text available: pdf(597.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

An artificial intelligence-based design automation methodology for computer-generated design of electric circuit is proposed. Given the requirements of the circuit to be designed, a program using this approach can generate a set of candidate circuits, evaluate them according to the specified requirements, and recommend the most suitable circuit(s). A PROLOG program (called VMD) implementing this approach has been developed for the design of voltage multiplier circuits. Issues pertaining to ...



6 Proceedings of the IEEE symposium on Parallel rendering



James Painter, Gordon Stoll, Kwa-Liu Ma

October 1997 proceeding

Publisher: ACM Press

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[Global Telecommunications Conference, 1991. GLOBECOM '91. Countdown to the New Millennium. Featuring a Mini-Theme on: Personal Communications Services](#)
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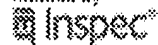
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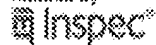
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